

ONE PAGE BIODATA

<b>Name</b>	<b>Rekha Yadav</b>			
<b>Designation</b>	Assitant Professor			
<b>Pay Scale</b>	15600-39100 + 6000			
<b>Department</b>	Electronics & Communication Engineering Department			
<b>Qualification</b>	Pursuing Ph. D. (Electronics & Communication Engineering) M.Tech. (VLSI Design & Embedded System) B. E. (Electronics & Communication Engineering)			
<b>Date of joining</b>	<b>University</b> 05.02.2010	<b>Present Post</b> 05.02.2010		
<b>Experience (in years)</b>	<b>Total</b> 8 Years	<b>Teaching</b> 8 Years	<b>Industry</b> 0 Years	
<b>Research Interests</b>	Analog & Digital VLSI Design, RF IC Design			
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<b>Research Publications</b>	<b>International Journals</b>	<b>National Journals</b>	<b>International Conferences</b>	<b>National Conferences</b>
	10	1	4	6
<b>Books/Chapters Published</b>	0			
<b>Research Guidance</b>	<b>Ph. D.</b>	00	<b>Masters</b>	12
<b>Research Projects</b>	<b>Completed</b>	00	<b>Inprogress</b>	00
<b>Consultancy Projects</b>	<b>Completed</b>	00	<b>Inprogress</b>	00
<b>Seminar/Conference/STTPs</b>	<b>Attended</b>	15	<b>Organized</b>	8
<b>Professional Affiliations</b>	Member – ISTE Associate Member – IETE			
<b>Awards/ Fellowships etc.</b>				